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for professionals with a background in systems engineering or digital signal processing.

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باتک دولناد Top-Down Digital VLSI Design by Hubert Kaeslin

Top-Down Digital VLSI Design, From Architectures to Gate-Level Circuits and FPGAs. by Hubert Kaeslin. Morgan Kaufmann Publishers, Boston. ISBN 978-0-12-800730-3. This book was written by Prof. Hubert Kaeslin, former head of Microelectronics Design Center, who has established the VLSI lecture series at ETH Zurich and held the VLSI lecture series for more than 30 years.

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